

FEB 12 2002

<b>FORM PTO-1449</b> U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office	<b>ATTY. DOCKET NO.</b> <b>500.33021CX5</b>	<b>TRADEMARK SERIAL NO.</b> <b>09/933,805</b>
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		
(Use several sheets if necessary)		
<b>APPLICANT</b> <b>NINOMIYA, et al</b>		<b>FILING DATE</b> <b>August 22, 2001</b>

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT
HBP	3	3	1	9	1	9		2/91	Japan			XX
HBP	4	2	8	4	5	2	3	10/92	Japan			XX
HBP	4	2	6	4	9	1	0	9/92	Japan			XX
HBP	4	9	6	8	1	8		3/92	Japan			XX
HBP	3	1	9	2	4	2	5	8/91	Japan			XX
HBP	4	6	0	8	4	6		2/92	Japan			XX
HBP	4	2	2	3	5	1	8	8/92	Japan			XX

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**EXAMINER**

H.B. Patel

**DATE CONSIDERED**

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(Form PTO-1449 [6-4])

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													YES	NO	
HBP		5	5	3	9	3	6		3/93	Japan				XX	
HBP		5	1	3	4	9	4	5	6/93	Japan				XX	
HBP		4	1	5	5	4	6	6	5/92	Japan				XX	
HBP		3	2	4	5	2	4	6	10/91	Japan				XX	
HBP		4	1	5	7	5	4	9	5/92	Japan				XX	
HBP		2	2	3	4	2	5	4	9/90	Japan				XX	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

M. Adams, et al "Database Computer Based on a Multi-microprocessor System, Final Report", DIALOG(R) File 6:NTIS, Comp&Distr. 2000 NTIS, Intl., pp. 11-13.

"Using Dual and Mappable Spare Bus" IBM Technical Disclosure Bulletin, Feb. 1994, pp 59-64

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